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heseby certify that this correspondence is being deposited with the U.S. Postal space with sufficient postage as First Class Mail, in arrenvelope addressed to: Dominissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the

Dated: March 9, 2004

Lang

Docket No.: INTECH 3.0-097 03 P 50758 US

Group Art Unit: 2818

Examiner: Not Yet Assigned

(PATENT)

IN THE UNITED STATES PÁTENT AND TRADEMARK OFFICE

In re Patent Application of:

Peter Poechmueller

Serial No.: 10/625,962

20110111011110111101111

Filed: July 24, 2003

For: ARRAY TRANSISTOR AMPLIFICATION

METHOD AND APPARATUS FOR DYNAMIC

RANDOM ACCESS MEMORY

SUBMISSION OF FORMAL DRAWINGS

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Enclosed herewith are eight (8) sheets of formal drawings for filing in connection with the above-identified patent application. The formal drawings are submitted in order to correct the informalities listed in form PTO-948.

In the event there are any fees due or owing in connection with this matter, please charge same to our Deposit Account No. 12-1095.

Dated: March 9, 2004

Respectfully submitted,

Daryl K. Neff

Registration No.: 38,253

LERNER, DAVID, LITTENBERG, KRUMHOLZ & MENTLIK, LLP

600 South Avenue West

Westfield, New Jersey 07090

(908) 654-5000

Attorney for Applicant

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